

Fig. 1

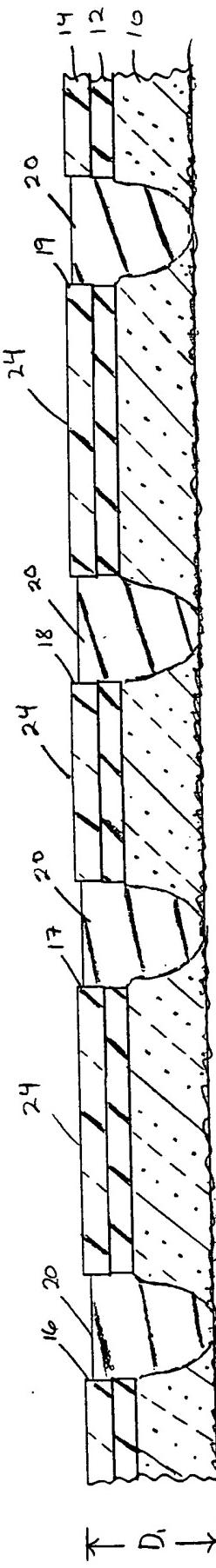


Fig. 2

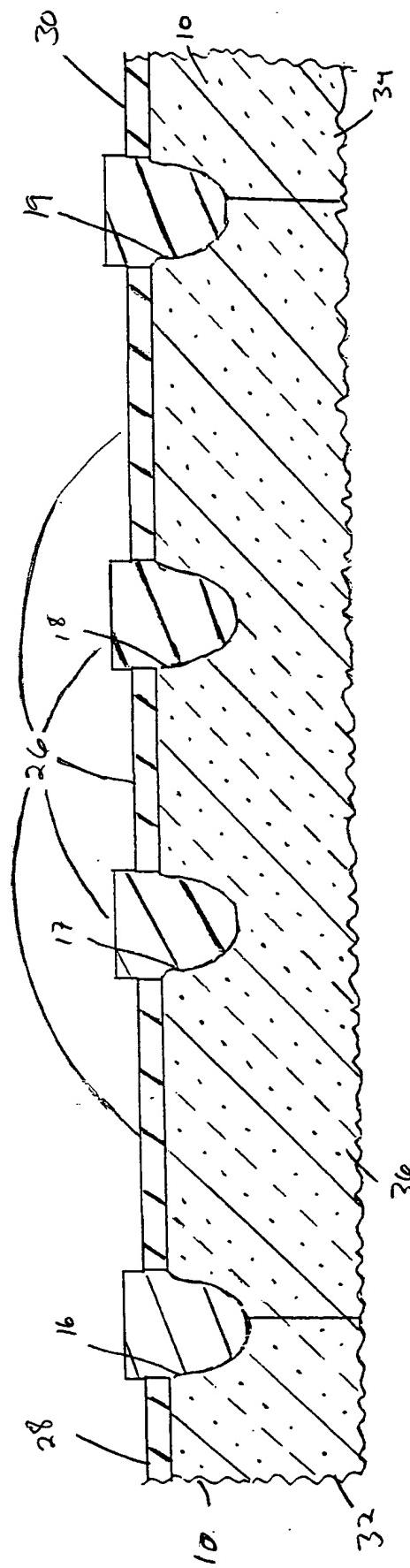


Fig. 3

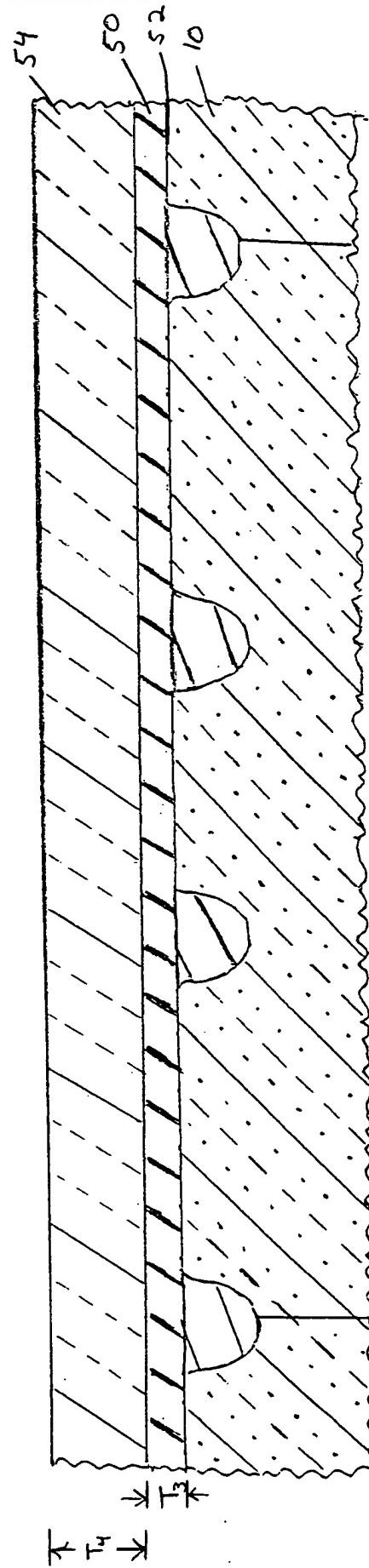


Fig. 4

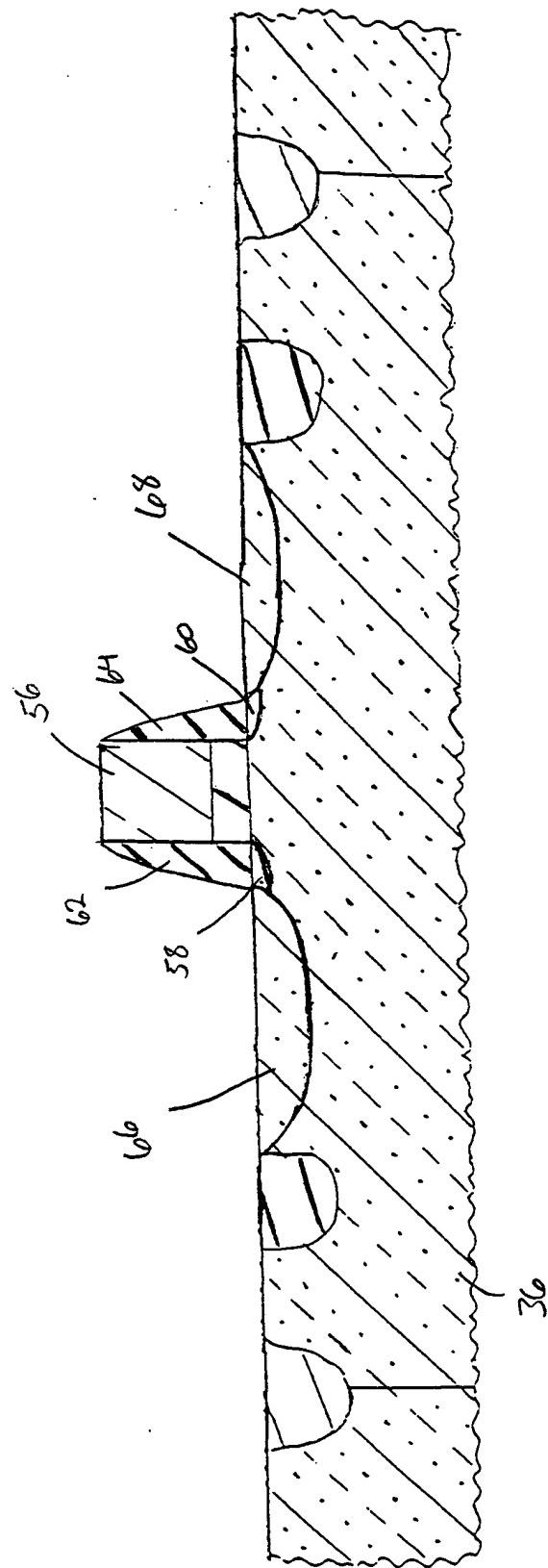


Fig. 5

Applicant(s): Been-Yih Jin et al.

# NON-SI<sub>X</sub> ON SEMICONDUCTOR AND HIGH-K GATE DIELECTRIC METAL OXIDE SEMICONDUCTOR FIELD-EFFECT TRANSISTORS

